

Form PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

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Doctor Number (Copies)

DS-02-021

Application Number

10/615, 123

Applicant

Rainer Krenzke

Filing Date

07/08/03

Group Art Unit

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

MIT	-	U.S. Patent App. Pub. US 2002/0063590 A1 to Nanba et al. Pub. Date 5/30/02, U.S. Class 327/170, Filed 8/6/01.

ΕΛΛΗΝΙΚΗ

my brother

DATE CONSIDERED

04/27/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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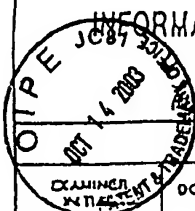
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U. S. PATENT DOCUMENTS

EXAMINER IN PATENT & TRADEMARK OFFICE	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
MT	6181193	1/30/01	Coughlin, Jr.	327	534	10/8/99
MT	6201417	3/13/01	Blum et al.	327	14	9/2/94
MT	6476654	11/5/02	Tanaka	327	170	7/12/01
MT	6483386	11/19/02	Cress et al.	330	298	9/29/00

FOREIGN PATENT DOCUMENTS

EXAMINER	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

MT	DS-02-020 to Rainer Krenzke, Serial # 10/614,663, Filed 07/07/03, "Comparator with High-Voltage Inputs in an Extended CMOS Process for Higher Voltage Levels."

EXAMINER

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